

[Insert title of invention]
INTEGRATED CIRCUIT HAVING
MEMORY DISPOSED THEREON
AND METHOD OF MAKING
THEREOF

Abstract of Disclosure

An integrated circuit having memory disposed thereon and method of making thereof includes a standard dimension carrier substrate and an information router integrated on the carrier substrate. Further included therein is at least one system memory integrated on the carrier substrate and in electrical communication with the information router across at least one of the electrical leads associated with the carrier substrate. Thereupon, system instructions may be stored and retrieved from the system memory through the information router within the integrated circuit on the standard dimension carrier substrate

Figures